

Day : Friday
Date: 8/24/2007

Time: 16:08:18

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = VARANASI

First Name = SURYA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09098657</u>	<u>6301304</u>	150	06/17/1998	ARCHITECTURE AND METHOD FOR INVERSE QUANTIZATION OF DISCRETE COSINE TRANSFORM COEFFICIENTS IN MPEG DECODERS	VARANASI, SURYA
<u>10698851</u>	<u>Not Issued</u>	30	10/31/2003	Frame traffic balancing across trunk groups	VARANASI, SURYA
<u>10699567</u>	<u>Not Issued</u>	41	10/31/2003	Logical ports in trunking	VARANASI, SURYA
<u>10699568</u>	<u>Not Issued</u>	41	10/31/2003	Load balancing in core-edge configurations	VARANASI, SURYA
<u>10767213</u>	<u>Not Issued</u>	30	01/29/2004	Fibre channel zoning hardware for directing a data packet to an external processing device	VARANASI, SURYA P.
<u>08903809</u>	<u>6122316</u>	150	07/31/1997	MPEG DECODING SYSTEM MEETING 2-FRAME STORE AND LETTERBOXING REQUIREMENTS	VARANASI, SURYA P.
<u>08904084</u>	<u>6289053</u>	150	07/31/1997	ARCHITECTURE FOR DECODING MPEG COMPLIANT VIDEO BITSTREAMS MEETING 2-FRAME AND LETTERBOXING REQUIREMENTS	VARANASI, SURYA P.
<u>08904085</u>	<u>6504871</u>	150	07/31/1997	IDCT PROCESSOR FOR USE IN DECODING MPEG COMPLIANT VIDEO BITSTREAMS MEETING 2-FRAME AND LETTERBOXING REQUIREMENTS	VARANASI, SURYA P.
<u>08904086</u>	<u>6236681</u>	150	07/31/1997	METHOD FOR DECODING	VARANASI, SURYA

				MPEG COMPLIANT VIDEO BITSTREAMS MEETING 2-FRAME AND LETTERBOXING REQUIREMENTS	P.
<u>08904088</u>	6101221	150	07/31/1997	VIDEO BITSTREAM SYMBOL EXTRACTOR FOR USE IN DECODING MPEG COMPLIANT VIDEO BITSTREAMS MEETING 2-FRAME AND LETTERBOXING REQUIREMENTS	VARANASI, SURYA P.
<u>08920511</u>	6266091	150	08/29/1997	SYSTEM AND METHOD FOR LOW DELAY MODE OPERATION VIDEO DECODING	VARANASI, SURYA P.
<u>10767410</u>	Not Issued	30	01/29/2004	Supplementary header for multifabric and high port count switch support in a fibre channel network	VARANASI, SURYA PRAKASH

Inventor Search Completed: No Records to Display.

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Your Search was:

Last Name = KO

First Name = KUNG-LING

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08308337	5590358	150	09/16/1994	PROCESSOR WITH WORD-ALIGNED BRANCH TARGET IN A BYTE-ORIENTED INSTRUCTION SET	KO, KUNG-LING
10667081	Not Issued	71	09/18/2003	Virtual channel remapping	KO, KUNG-LING
10698851	Not Issued	30	10/31/2003	Frame traffic balancing across trunk groups	KO, KUNG-LING
10699568	Not Issued	41	10/31/2003	Load balancing in core-edge configurations	KO, KUNG-LING
10767410	Not Issued	30	01/29/2004	Supplementary header for multifabric and high port count switch support in a fibre channel network	KO, KUNG-LING

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